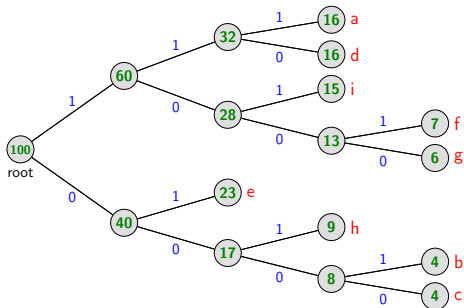


Lossless Coding IV

a_k	p_k	b_k
a	0.16	111
b	0.04	0001
c	0.04	0000
d	0.16	110
e	0.23	01
f	0.07	1001
g	0.06	1000
h	0.09	001
i	0.15	101



Shannon-Fano-Elias Coding: Review

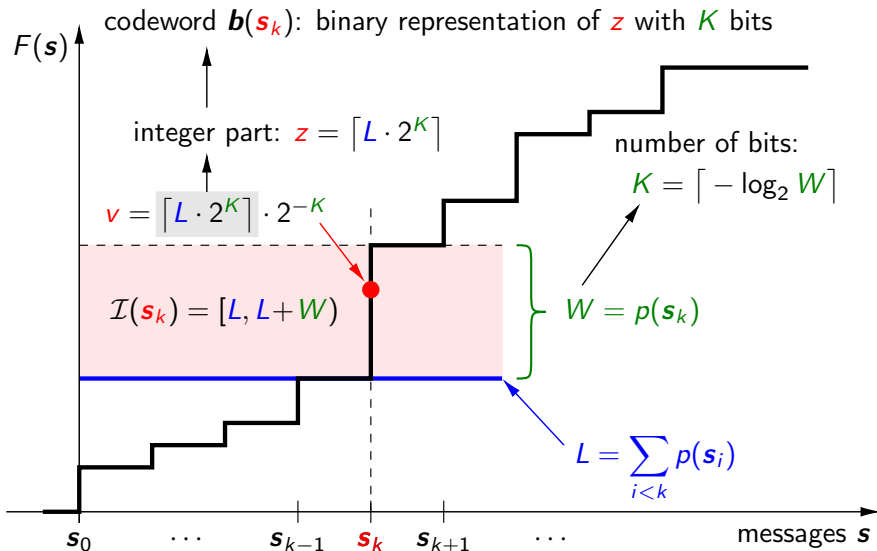
Shannon-Fano-Elias Coding: Special Block Code of size N

- Order of N -symbol sequences $\{\mathbf{s}_k\}$ is known by encoder and decoder
- N -th order pmf $p(\mathbf{s}_k) = P(\mathbf{S} = \mathbf{s}_k)$ is known by encoder and decoder
- ➔ On-the-fly encoding and decoding (no codeword table)

Basic Concept

- Unique mapping of N -symbol sequences to intervals \mathcal{I}_k of cdf $F(\mathbf{s})$
- Half-open intervals $\mathcal{I}_k = [L_k, U_k) = [L_k, L_k + W_k)$ are characterized by
 - Interval width: $W_k = F(\mathbf{s}_k) - F(\mathbf{s}_{k-1}) = p(\mathbf{s}_k)$
 - Lower interval boundary: $L_k = F(\mathbf{s}_{k-1}) = \sum_{i < k} p(\mathbf{s}_i)$
- Codewords: Fractional bits of representative value v_k inside interval \mathcal{I}_k
 - Length of codeword: $K = \lceil -\log_2 W_k \rceil$
 - Representative value: $v_k = \lceil L_k \cdot 2^K \rceil \cdot 2^{-K} = z_k \cdot 2^{-K}$
- ➔ Codeword: Binary representation of $z_k = \lceil L_k \cdot 2^K \rceil$ with K bits

Shannon-Fano-Elias Encoding: Illustration



Shannon-Fano-Elias Encoding: Summary

Determination of Codewords

- Given: Ordered set of symbol sequences $\{\mathbf{s}_k\}$ with associated pmf $\{p_k\}$
- Construct codeword $\mathbf{b}_k = \mathbf{b}(\mathbf{s}_k)$ for any particular sequence \mathbf{s}_k by

- 1 Determine interval width W_k and lower interval boundary L_k

$$W_k = p_k \quad (1)$$

$$L_k = \sum_{i < k} p_i \quad (2)$$

- 2 Determine codeword length K_k

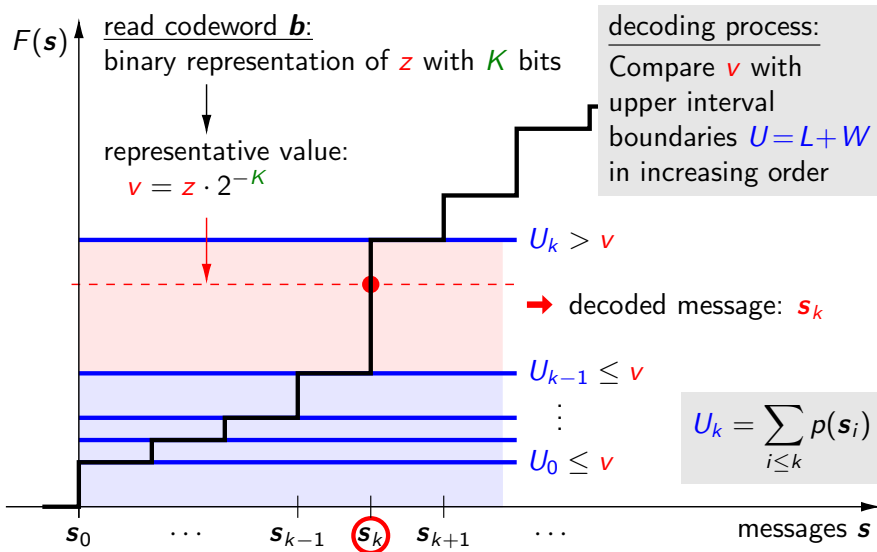
$$K_k = \lceil -\log_2 W_k \rceil \quad (3)$$

- 3 Determine representative integer z_k

$$z_k = \lceil L_k \cdot 2^{K_k} \rceil \quad (4)$$

- 4 Codeword \mathbf{b}_k : Binary representation of z_k with K_k bits

Shannon-Fano-Elias Decoding: Illustration



Shannon-Fano-Elias Decoding: Summary

Decoding of a Symbol Sequence

- Given: Ordered set of symbol sequences $\{\mathbf{s}_k\}$ with associated pmf $\{p_k\}$

1 Read codeword \mathbf{b} :

Codeword \mathbf{b} has K bits and represents the binary value of the integer z

2 Determine the representattive value v according to

$$v = z \cdot 2^{-K} \quad (5)$$

3 Initilization: Set index $k = 0$ and upper interval boundary $U_0 = p_0$

4 Compare v with U_k

- If $v < U_k$
 - ➔ Output decoded symbol sequence \mathbf{s}_k
 - ➔ Terminate decoding
- Otherwise ($v \geq U_k$)
 - ➔ Set $k = k + 1$ and $U_k = U_{k-1} + p_k$
 - ➔ Goto step **4**

Example for a Shannon-Fano-Elias Code

Blocks of three Symbols for a Binary IID Source

→ Binary iid source with alphabet $\mathcal{A} = \{a, b\}$ and pmf $p = \{0.8, 0.2\}$

joint pmf		intervals		number		codeword
s_k	p_k	W_k	L_k	K_k	z_k	b_k
aaa	0.512	0.512	0.000	1	0	0
aab	0.128	0.128	0.512	3	5	101
aba	0.128	0.128	0.640	3	6	110
abb	0.032	0.032	0.768	5	25	11001
baa	0.128	0.128	0.800	3	7	111
bab	0.032	0.032	0.928	5	30	11110
bba	0.032	0.032	0.960	5	31	11111
bbb	0.008	0.008	0.992	7	127	1111111

average codeword length: $\bar{\ell} = 0.733$

block Huffman code: $\bar{\ell} = 0.728$

$$W_k = p_k$$

$$L_k = \sum_{i < k} p_i$$

$$K_k = \lceil -\log_2 W_k \rceil$$

$$z_k = \lceil L_k \cdot 2^{K_k} \rceil$$

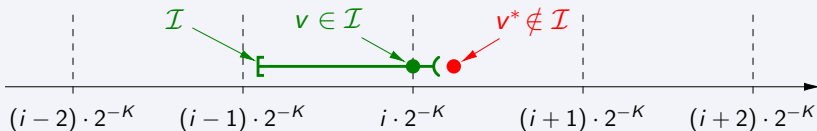
b_k : z_k with K_k bits

→ Worse than block Huffman code for same block size ($N = 3$)

→ **Code is not prefix-free !** → Can be a problem (depends on application) !

Why Is The Code Not Prefix-Free ?

Effect of Codeword Concatenation



- Encoder transmits codeword $\mathbf{b} = \{b_0, b_1, b_2, \dots, b_{K-1}\}$ of K bits, signaling the binary fraction $v \in \mathcal{I}$

$$v = (0.b_0b_1b_2 \dots b_{K-1})_b$$

- Decoder sees a modified binary fraction v^* given by

$$v^* = (0.b_0b_1b_2 \dots b_{K-1}b_Kb_{K+1}b_{K+2} \dots)_b$$

where $\{b_Kb_{K+1}b_{K+2} \dots\}$ are the bits of following codewords

- Depending on the location of v inside the interval \mathcal{I} and the values of the following bits, v^* can lay outside the interval \mathcal{I}

Prefix-Free Shannon-Fano-Elias Code

Prefix Code Construction

→ Ensure that binary fraction v^* (seen by decoder) lies inside interval

- Worst case: All following bits are equal to 1

$$v^* = v + \sum_{i=K+1}^{\infty} 2^{-i} < L + W \quad (6)$$

- Since the sum in above equation is less than 2^{-K} , we require

$$v^* < v + 2^{-K} \leq L + W \quad (7)$$

- Question: How many bits K do we need for representing v according to

$$v = \lceil L \cdot 2^K \rceil \cdot 2^{-K}$$

→ Have to choose K so that the following inequality is fulfilled

$$v + 2^{-K} = \lceil L \cdot 2^K \rceil \cdot 2^{-K} + 2^{-K} \leq L + W \quad (8)$$

Prefix-Free Shannon-Fano-Elias Code

Prefix Code Construction (continued)

- Since $\lceil x \rceil < x + 1$, the inequality

$$\lceil L \cdot 2^K \rceil \cdot 2^{-K} + 2^{-K} \leq L + W$$

is always fulfilled if we have

$$(L \cdot 2^K + 1) \cdot 2^{-K} + 2^{-K} \leq L + W$$

$$L + 2 \cdot 2^{-K} \leq L + W$$

$$2^{1-K} \leq W$$

$$1 - K \leq \log_2 W$$

$$K \geq 1 - \log_2 W \quad (9)$$

- Unique decodability is guaranteed, if we choose

$$K = \lceil 1 - \log_2 W \rceil = \lceil -\log_2 W \rceil + 1 \quad (10)$$

- One additional bit per codeword (compared to non-prefix-free version)

Example for a Prefix-Free Shannon-Fano-Elias Code

Repeated Example: Blocks of three Symbols for a Binary IID Source

→ Binary iid source with alphabet $\mathcal{A} = \{a, b\}$ and pmf $p = \{0.8, 0.2\}$

joint pmf		intervals		number		codewords
s_k	p_k	W_k	L_k	K_k	z_k	b_k
aaa	0.512	0.512	0.000	2	0	00
aab	0.128	0.128	0.512	4	9	1001
aba	0.128	0.128	0.640	4	11	1011
abb	0.032	0.032	0.768	6	50	110010
baa	0.128	0.128	0.800	4	13	1101
bab	0.032	0.032	0.928	6	60	111100
bba	0.032	0.032	0.960	6	62	111110
bbb	0.008	0.008	0.992	8	254	11111110

$$W_k = p_k$$

$$L_k = \sum_{i < k} p_i$$

$$K_k = \lceil 1 - \log_2 W_k \rceil$$

$$z_k = \lceil L_k \cdot 2^{K_k} \rceil$$

b_k : z_k with K_k bits

average codeword length: $\bar{\ell} = 1.067$

block Huffman code: $\bar{\ell} = 0.728$

→ Additional bit ensures that code becomes a **prefix code**

→ Worse than block Huffman code (several **redundant bits**)

Efficiency of Shannon-Fano-Elias Codes

Average Codeword Length

- Average codeword length $\bar{\ell}$ per symbol

$$\bar{\ell} = \frac{\mathbb{E}\{K(\mathbf{S})\}}{N} = \frac{\mathbb{E}\{ \lceil A - \log_2 p_N(\mathbf{S}) \rceil \}}{N} \quad \text{with} \quad A = \begin{cases} 1 & \text{: prefix-free} \\ 0 & \text{: otherwise} \end{cases} \quad (11)$$

Bounds on Average Codeword Length

- Using inequalities $x \leq \lceil x \rceil$ and $\lceil x \rceil < x + 1$, we obtain

$$\begin{aligned} \frac{\mathbb{E}\{-\log_2 p_N(\mathbf{S})\}}{N} + \frac{A}{N} &\leq \bar{\ell} < \frac{\mathbb{E}\{-\log_2 p_N(\mathbf{S})\}}{N} + \frac{1+A}{N} \\ \frac{H_N(\mathbf{S})}{N} + \frac{A}{N} &\leq \bar{\ell} < \frac{H_N(\mathbf{S})}{N} + \frac{1+A}{N} \end{aligned} \quad (12)$$

- ➔ Non-prefix-free version ($A = 0$): Same bounds as for block Huffman coding
- ➔ Both versions: Close to entropy rate for $N \gg 1$ (for typical sources)

Shannon-Fano-Elias Coding: Intermediate Results

Shannon-Fano-Elias Code

- Special block code (for given number of symbols N)
- Worse than block Huffman code of same size N
- Still close to entropy bound H_N/N for $N \gg 1$
- **No need to store codeword table !**
- **Have to store N -th order pmf (or N -th order cdf) !**

What is the advantage ?

Iterative Coding

- Can define a suitable order for sequences of N symbols
- Probability intervals are nested
- Iterative calculation of interval boundaries
- Iterative codeword construction

Sorting of Symbol Sequences

Lexicographical Order

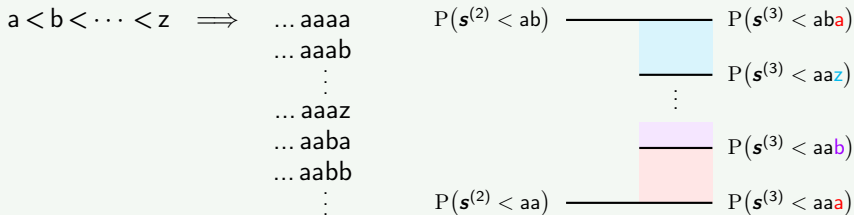
- Define any order for single symbols s_k (sorted symbol alphabet)
- Consider two sequences of N symbols

$$\mathbf{s}_a = \{s_0^a, s_1^a, \dots, s_{N-1}^a\} \quad \text{and} \quad \mathbf{s}_b = \{s_0^b, s_1^b, \dots, s_{N-1}^b\}$$

→ Lexicographical order for sequences of N symbols

$$\mathbf{s}_a < \mathbf{s}_b \iff \exists n < N : \left(\forall k < n : s_k^a = s_k^b \right) \wedge \left(s_n^a < s_n^b \right) \quad (13)$$

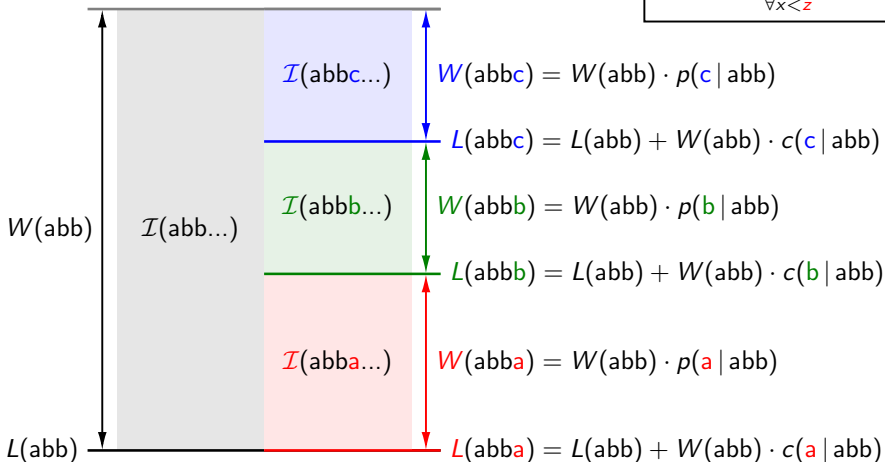
Example: Alphabetical Order



Iterative Interval Refinement: Illustration

Example: 3-symbol alphabet $\mathcal{A} = \{a, b, c\}$

$$c(z | ..) = \sum_{\forall x < z} p(x | \dots)$$



Important: Probability intervals are nested!

Iterative Interval Refinement: Interval Width

Interval Refinement

- Consider sub-sequences $\mathbf{s}^{(n)} = \{s_0, s_1, \dots, s_{n-1}\}$ with $n < N$
- ➔ Determine interval $\mathcal{I}_{n+1} = [L_{n+1}, L_{n+1} + W_{n+1})$ for $\mathbf{s}^{(n+1)} = \{\mathbf{s}^{(n)}, s_n\}$ based on interval $\mathcal{I}_n = [L_n, L_n + W_n)$ for prefix sequence $\mathbf{s}^{(n)}$

Refinement of Interval Width

- Interval width W_{n+1} for sub-sequence $\mathbf{s}^{(n+1)} = \{\mathbf{s}^{(n)}, s_n\}$

$$\begin{aligned}
 W_{n+1} &= P\left(\mathbf{S}^{(n+1)} = \mathbf{s}^{(n+1)}\right) \\
 &= P\left(\mathbf{S}^{(n)} = \mathbf{s}^{(n)}, S_n = s_n\right) \\
 &= P\left(\mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \cdot P\left(S_n = s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right)
 \end{aligned} \tag{14}$$

- ➔ Iteration rule for interval width

$$W_{n+1} = W_n \cdot p(s_n \mid s_{n-1}, s_{n-2}, \dots, s_0) \tag{15}$$

Iterative Interval Refinement: Lower Interval Boundary

Refinement of Lower Interval Boundary

- Lower interval boundary L_{n+1} for sub-sequence $\mathbf{s}^{(n+1)} = \{\mathbf{s}^{(n)}, s_n\}$

$$\begin{aligned}
 L_{n+1} &= P\left(\mathbf{S}^{(n+1)} < \mathbf{s}^{(n+1)}\right) \\
 &= P\left(\mathbf{S}^{(n)} < \mathbf{s}^{(n)}\right) + P\left(\mathbf{S}^{(n)} = \mathbf{s}^{(n)}, S_n < s_n\right) \\
 &= P\left(\mathbf{S}^{(n)} < \mathbf{s}^{(n)}\right) + P\left(\mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \cdot P\left(S_n < s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \quad (16)
 \end{aligned}$$

- Define **modified cmf** $c(\cdot)$, which excludes current symbol

$$c(s_n \mid s_{n-1}, \dots, s_0) = P\left(S_n < s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) = \sum_{\forall a < s_n} p(a \mid s_{n-1}, \dots, s_0) \quad (17)$$

→ Iteration rule for lower interval boundary

$$L_{n+1} = L_n + W_n \cdot c(s_n \mid s_{n-1}, s_{n-2}, \dots, s_0) \quad (18)$$

Nested Probability Intervals: Verification

Intervals are Nested

- Lower interval boundary

$$L_{n+1} = L_n + W_n \cdot \mathbb{P}\left(S_n < s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \geq L_n \quad (19)$$

- Upper interval boundary

$$\begin{aligned} L_{n+1} + W_{n+1} &= L_n + W_n \cdot \mathbb{P}\left(S_n < s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \\ &\quad + W_n \cdot \mathbb{P}\left(S_n = s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \\ &= L_n + W_n \cdot \mathbb{P}\left(S_n \leq s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \\ &= L_n + W_n - W_n \cdot \mathbb{P}\left(S_n > s_n \mid \mathbf{S}^{(n)} = \mathbf{s}^{(n)}\right) \\ &\leq L_n + W_n \end{aligned} \quad (20)$$

➔ Interval for $\mathbf{s}^{(n+1)} = \{\mathbf{s}^{(n)}, s_n\}$ is fully included in interval for $\mathbf{s}^{(n)}$

Iterative Interval Refinement

Iterative Algorithm for Calculation of Interval Boundaries

■ Initialization:
$$W_0 = 1 \quad (21)$$

$$L_0 = 0 \quad (22)$$

■ Iteration Step:
$$W_{n+1} = W_n \cdot p(s_n | s_{n-1}, \dots, s_0) \quad (23)$$

$$L_{n+1} = L_n + W_n \cdot c(s_n | s_{n-1}, \dots, s_0) \quad (24)$$

Advantage of Interval Refinement ?

■ Derived iteration approach:

- Conditional pmf $p(s_n | s_{n-1}, \dots)$ instead of joint pmf $p(s_0, \dots, s_{N-1})$

→ Need to store same amount of data

→ But: Conditional pmfs can be well approximated using simple models

- IID model:
$$p(s_n | s_{n-1}, \dots, s_0) = p(s_n)$$

- Markov model:
$$p(s_n | s_{n-1}, \dots, s_0) = p(s_n | s_{n-1})$$

Practical Iterative Interval Refinement

Simplified Iteration

■ IID Model:
$$W_{n+1} = W_n \cdot p(s_n) \quad (25)$$

$$L_{n+1} = L_n + W_n \cdot c(s_n) \quad (26)$$

■ Markov Model:
$$W_{n+1} = W_n \cdot p(s_n | s_{n-1}) \quad (27)$$

$$L_{n+1} = L_n + W_n \cdot c(s_n | s_{n-1}) \quad (28)$$

- Many other simple models possible: $\text{Condition} = f(s_{n-1}, \dots)$

Other Aspects

- Switching between symbol alphabets possible
 - Suitable for complicated syntax (as for prefix codes)
- Adaptation of probability models
 - Probabilities can be estimated during encoding and decoding
 - Elegant way to deal with instationary or unknown sources

Iterative Encoding Algorithm

Iterative Shannon-Fano-Elias Encoding

1 Given is a sequence $\mathbf{s} = \{s_0, s_1, s_2, \dots, s_{N-1}\}$ of N symbols

2 Initialization of probability interval

$$W_0 = 1 \quad \text{and} \quad L_0 = 0 \quad (29)$$

3 Determine probability interval: For each $n = 0, 1, \dots, N - 1$, do

$$W_{n+1} = W_n \cdot p(s_n | \dots) \quad (30)$$

$$L_{n+1} = L_n + W_n \cdot c(s_n | \dots) \quad (31)$$

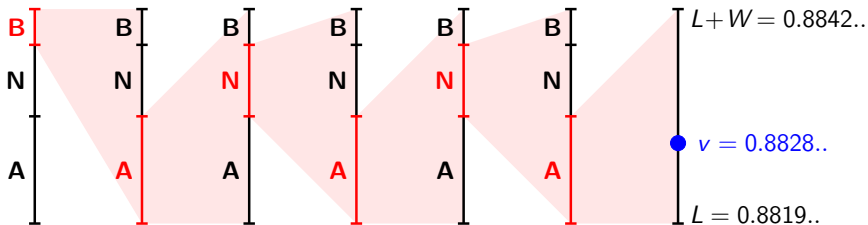
4 Determine codeword length and codeword value

$$K = \lceil -\log_2 W_N \rceil \quad (\text{for prefix code: } K \rightarrow K + 1) \quad (32)$$

$$z = \lceil L_N \cdot 2^K \rceil \quad (33)$$

5 Transmit codeword $\mathbf{b}(\mathbf{s})$: Binary representation of z with K bits

Iterative Encoding Example: IID Source



a	$p(a)$	$c(a)$
A	$\frac{1}{2}$	0
N	$\frac{1}{3}$	$\frac{1}{2}$
B	$\frac{1}{6}$	$\frac{5}{6}$

	init	B	A	N	A	N	A
W_n	1	$\frac{1}{6}$	$\frac{1}{12}$	$\frac{1}{36}$	$\frac{1}{72}$	$\frac{1}{216}$	$\frac{1}{432}$
L_n	0	$\frac{5}{6}$	$\frac{5}{6}$	$\frac{21}{24}$	$\frac{21}{24}$	$\frac{127}{144}$	$\frac{127}{144}$

$$K = \lceil -\log_2 W \rceil = \lceil \log_2 432 \rceil = 9$$

$$W_{n+1} = W_n p(s_n)$$

$$z = \lceil L \cdot 2^K \rceil = \lceil \frac{127}{144} \cdot 512 \rceil = 452 \quad \left(v = \frac{452}{512} \right)$$

$$L_{n+1} = L_n + W_n c(s_n)$$

$$\mathbf{b} = \text{"111000100"} \quad (z = 452 \text{ with } K = 9 \text{ bits})$$

Iterative Decoding Algorithm

Iterative Shannon-Fano-Elias Decoding

- 1 Given:
 - Bitstream $\mathbf{b} = \{b_0, b_1, \dots, b_{K^*-1}\}$ of $K^* \geq K$ bits
 - Number N of symbols to be decoded
- 2 Determine interval representative: $v = (0.b_0b_1 \dots b_{K^*-1})_b = z \cdot 2^{-K^*}$
- 3 Initialization of probability interval: $W_0 = 1$ and $L_0 = 0$
- 4 Iterative decoding: For each $n = 0, 1, \dots, N - 1$, do

- a Calculate symbol intervals: For each $a \in \mathcal{A}_n$, calculate

$$W_{n+1}(a) = W_n \cdot p(a | \dots) \quad (34)$$

$$L_{n+1}(a) = L_n + W_n \cdot c(a | \dots) \quad (35)$$

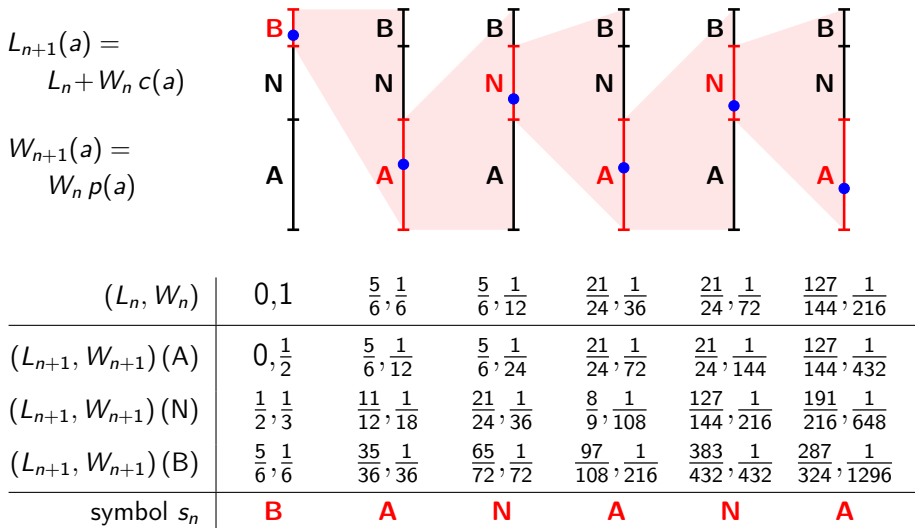
- b Compare v with upper interval boundaries in increasing symbol order and output symbol s_n with

$$L_{n+1}(s_n) \leq v < U_{n+1}(s_n) = L_{n+1}(s_n) + W_{n+1}(s_n) \quad (36)$$

- c Update interval boundary and interval width

$$W_{n+1} = W_{n+1}(s_n) \quad \text{and} \quad L_{n+1} = L_{n+1}(s_n) \quad (37)$$

Iterative Decoding Example: IID Source



$$v = \frac{452}{512}$$

$$b = "111000100" \implies s = "BANANA"$$

Arithmetic Coding

Iterative Shannon-Fano-Elias Coding

→ Iterative Encoding and Decoding

- Iterative interval refinement
- Simple codeword construction

→ Precision requirements and delay for larger N

- Require extremely high precision for W_n , L_n , and v
- Encoder: Complete codeword written at end of encoding
- Decoder: Complete codeword read at start of decoding

Arithmetic Coding

- Fixed-precision approximation of Shannon-Fano-Elias coding
- Represent pmf(s) with fixed-precision integers
- Represent interval width with fixed-precision integers
- Output bits as soon as possible

Quantization of PMF(s)

Fixed-Precision Approximation of Pmf(s)

- Choose number of bits V for representing probability masses
- Represent probability masses $p(a)$ by **V -bit integers** $p_V(a)$

$$p(a) = p_V(a) \cdot 2^{-V} \quad (38)$$

→ Resulting modified cmf $c(a)$ can also be represented by **V -bit integers** $c_V(a)$

$$c(a) = \sum_{\forall b < a} p(b) = \left(\sum_{\forall b < a} p_V(b) \right) \cdot 2^{-V} = c_V(a) \cdot 2^{-V} \quad (39)$$

Requirements on Pmf Approximation

- Probability masses must be non-zero** and **pmf must be valid**

$$\forall a : p_V(a) > 0 \quad \text{and} \quad \sum_{\forall a} p_V(a) \leq 2^V \quad (40)$$

Quantization of Interval Width

Fixed-Precision Approximation of Interval Width

- Represent interval width W_n by U -bit integer A_n and counter $z_n \geq U$

$$W_n = A_n \cdot 2^{-z_n} \quad (41)$$

- Use maximum possible precision for A_n

- Restrict A_n according to

$$2^{U-1} \leq A_n < 2^U \quad (42)$$

- Use following initialization

$$A_0 = 2^U - 1, \quad z_0 = U \quad \iff \quad W_0 = 1 - 2^{-U} \quad (43)$$

- Binary representation of interval width $W_n = A_n \cdot 2^{-z_n}$

$$W_n = 0.\overbrace{00000 \cdots 0}^{z_n \text{ bits}} \underbrace{1xx \cdots x}_U 000 \cdots$$

$\underbrace{\hspace{10em}}_{z_n - U \text{ bits}} \quad \underbrace{\hspace{5em}}_{U \text{ bits}}$

Rounding in Interval Refinement

Conventional Refinement of Interval Width

- Refinement of interval width

$$W_{n+1} = W_n \cdot p(s_n) \quad (44)$$

$$A_{n+1} \cdot 2^{-z_{n+1}} = \underbrace{(A_n \cdot p_V(s_n))}_{(U+V)\text{-bit integer}} \cdot 2^{-(z_n+V)} \quad (45)$$

- In general: $W_n \cdot p(s_n)$ cannot be represented using a U -bit integer
- **What can we do?**

Requirement for Unique Decodability

- Code remains uniquely decodable if we ensure

$$0 < W_{n+1} \leq W_n \cdot p(s_n) \quad (46)$$

- **Solution:** Rounding down of $W_n \cdot p(s_n)$ in each iteration so that W_{n+1} can be represented using $A_{n+1} \cdot 2^{-z_{n+1}}$ with $2^{U-1} \leq A_{n+1} < 2^U$

Refinement of Interval Width

Product of Interval Width and Pmf Entry

- Binary representations of $W_n = A_n \cdot 2^{-z_n}$ and $p(s_n) = p_V(s_n) \cdot 2^{-V}$

$$\begin{aligned}
 W_n &= 0.\overbrace{00000 \dots 0}^{z_n - U \text{ bits}} \overbrace{1xx \dots x}^{U \text{ bits}} 000 \dots \\
 p(s_n) &= 0.\underbrace{xxx \dots x}_{V \text{ bits}} 000 \dots
 \end{aligned}$$

- Interval refinement $W_n \cdot p(s_n) \mapsto W_{n+1}$

$$\begin{aligned}
 W_n \cdot p(s_n) &= 0.\overbrace{00000 \dots 0}^{z_n - U \text{ bits}} \overbrace{00 \dots 0}^{\Delta z \text{ bits}} \overbrace{1x \dots x}^{U \text{ bits}} \overbrace{xx \dots x}^{V - \Delta z \text{ bits}} 000 \dots \\
 &\qquad\qquad\qquad U + V \text{ bits: } A_n \cdot p_V(s_n) \\
 \downarrow \\
 W_{n+1} &= 0.\overbrace{00000 \dots 0}^{z_{n+1} - U \text{ bits}} \overbrace{00 \dots 0}^{\Delta z \text{ bits}} \overbrace{1x \dots x}^{U \text{ bits}} \overbrace{00 \dots 0}^{\text{red}} 000 \dots \\
 &\qquad\qquad\qquad A_{n+1}
 \end{aligned}$$

Effect on Binary Representations of Lower Interval Boundary

Product of Interval Width and Modified Cmf Entry

- Remember:

$$L_{n+1} = L_n + W_n \cdot c(s_n)$$

- Binary representations of $W_n = A_n \cdot 2^{-z_n}$ and $c(s_n) = c_V(s_n) \cdot 2^{-V}$

$$W_n = 0.\underbrace{00000 \dots 0}_{z_n - U \text{ bits}} \underbrace{1xx \dots x}_{U \text{ bits}} 000 \dots$$

$$c(s_n) = 0.\underbrace{xxx \dots x}_{V \text{ bits}} 000 \dots$$

- Binary representation of product $W_n \cdot c(s_n)$

$$W_n \cdot c(s_n) = 0.\underbrace{00000 \dots 0}_{z_n - U \text{ bits}} \underbrace{xxx \dots x}_{U + V \text{ bits}} 000 \dots$$

Effect on Binary Representation of Lower Interval Boundary

Update of Lower Interval Boundary

$$W_n \cdot c(s_n) = 0.\underbrace{00000 \dots 0}_{z_n - U \text{ bits}} \underbrace{\text{xxx} \dots \text{x}}_{U + V \text{ bits}} 000 \dots$$

- What is the effect on lower interval boundary?

$$L_n = 0.\underbrace{\underbrace{aaaaa \dots a}_{z_n - c_n - U}}_{\text{settled bits}} \underbrace{\underbrace{011111 \dots 1}_{c_n}}_{\text{outstanding bits}} \underbrace{\underbrace{\text{xxxxx} \dots \text{x}}_{U + V}}_{\text{active bits}} \underbrace{\underbrace{00000 \dots}}_{\text{trailing bits}}$$

- **Trailing bits:** Equal to 0, but maybe changed later
- **Active bits:** Directly modified by the update $L_{n+1} = L_n + W_n c(s_n)$
- **Outstanding bits:** May be modified by a carry from the active bits
- **Settled bits:** Not modified in any following interval update

Arithmetic Coding: Lower Interval Boundary & Output

Representation of Lower Interval Boundary

$$L_n = 0. \overbrace{\underbrace{aaaaa \cdots a}_{z_n - c_n - U} \underbrace{0111111 \cdots 1}_{c_n}}^{z_n - U \text{ bits}} \underbrace{xxxxx \cdots x}_{U+V} \underbrace{00000 \cdots}_{\text{trailing bits}}$$

settled bits
outstanding bits
active bits
trailing bits

■ Active bits:

- $(U+V)$ -bit integer B_n
- Intermediate value $B_n + A_n c_V(s_n)$ requires $(U+V+1)$ -bit integer

■ Outstanding bits:

- Counter c_n (trailing $c_n - 1$ bits are equal to 1)

■ Settled bits:

- Output as soon as they become settled

Arithmetic Coding: Interval Refinement

Update of Probability Interval

$$\begin{aligned}
 W_n &= 0. \overbrace{0000000000000000 \cdots 0}^{z_n - U} \overbrace{1xx \cdots x}^{A_n(U)} 000000000 \cdots \\
 L_n &= 0. \overbrace{aaaaa \cdots a}^{z_n - c_n - U} \overbrace{011 \cdots 1}^{c_n} \overbrace{xxx \cdots xxxxxx}^{B_n(U+V)} 00000 \cdots
 \end{aligned}$$

$$\begin{aligned}
 W_{n+1} &= 0. \overbrace{0000000000000000 \cdots 0}^{z_n - U} \overbrace{0 \cdots 0}^{\Delta z} \overbrace{1xx \cdots x}^{A_{n+1}(U)} 000000000 \cdots \\
 L_{n+1} &= 0. \overbrace{aaaaa \cdots a}^{z_n - c_n - U} \overbrace{xxxxxxxxx \cdots xxx}^{c_n + \Delta z} \overbrace{xxx \cdots xxxxxx}^{B_{n+1}(U+V)} 00000 \cdots
 \end{aligned}$$

Interval update

Δz = Number of trailing zeros in $(U+V)$ -bit integer $(A_n \cdot p_V(s_n))$

$$\text{mask} = (1 \ll (U + V - \Delta z)) - 1$$

$$A_{n+1} = (A_n \cdot p_V(s_n)) \gg (V - \Delta z)$$

$$B_{n+1} = ((B_n + A_n \cdot c_V(s_n)) \& \text{mask}) \ll \Delta z$$

Arithmetic Coding: Output of Settled Bits

Output of Bits

$$L_{n+1} = 0. \underbrace{aaaaa \cdots a}_{z_n - c_n - U} \underbrace{xxxxxxxxx \cdots xxx}_{c_n + \Delta z} \underbrace{xxx \cdots xxxxxx}_{B_{n+1} (U + V)} 00000 \cdots$$

- Investigate modified $(c_n + \Delta z)$ bits
- ➔ Update counter c_{n+1} and output new settled bits

Total Number of Bits for Arithmetic Codeword

- Total number of bits to output (note: $W_N = A_N 2^{-z_n}$)

$$K = \lceil -\log_2 W_N \rceil = z_n - \lfloor \log_2 A_N \rfloor = z_N - U + 1 \quad (47)$$

- ➔ Prefix-free version: $K = z_N - U + 2$
- Note: $z_N - U$ is the sum of settled and outstanding bits
 - ➔ Output c_N outstanding bits
 - ➔ Output one bit of B_N (prefix-free: two bits)

Termination of Arithmetic Codeword

Required Output

- Required output after last symbol was coded
 - c_N outstanding bits
 - most significant bit of B_N (prefix-free: two most significant bits)
- Note: Lower boundary must be rounded up to next multiple of 2^{-K}

Codeword Termination

- Set $n = 1$ (non-prefix free) or $n = 2$ (prefix-free)
- If the any of the last $(U + V - n)$ bits in B_N is equal to 1, do
 - Set $B_N = B_N + (1 \ll (U + V - n))$ (rounding up)
 - If $B_N \geq (1 \ll (U + V))$ (carry condition)
 - ▶ Invert outstanding bits, output new settled bits, set $c_N = 1$
 - ▶ Remove carry: $B_N = B_N - (1 \ll (U + V))$
- Output outstanding bits (one '0' and $(c_N - 1)$ times '1')
- Output n most significant bits of B_N

Overview of Arithmetic Encoding Process

Arithmetic Encoding

1 Initialization: $A_0 = 2^U - 1$, $B_0 = 0$, $c_0 = 0$

2 Iteration: For $n = 0, 1, \dots, N - 1$, do

a Calculate: $A^* = A_n \cdot p_V(s_n)$ ($U + V$ bits)
 $B^* = B_n + A_n \cdot c_V(s_n)$ ($U + V + 1$ bits)

b Determine number Δz of trailing zeros in $(U + V)$ -bit integer A^*

c Update: $\text{mask} = (1 \ll (U + V - \Delta z)) - 1$

$$A_{n+1} = A^* \gg (V - \Delta z)$$

$$B_{n+1} = (B^* \& \text{mask}) \ll \Delta z$$

d Determine outstanding bits counter c_{n+1} (based on c_n and B^*)

e Output new settled bits ($c_n + \Delta z - c_{n+1}$ bits)

3 Termination:

- Round up B_N
- Output c_N outstanding bits + one/two most significant bit(s) of B_N

Arithmetic Coding Example

Example: Preparation

- Coding example
 - IID source with symbol alphabet $\{A, N, B\}$
 - Pmf is given by $\{1/2, 1/3, 1/6\}$
 - Consider arithmetic coding with $V = 4$ and $U = 4$
 - Symbol sequence “BANANA”

- Preparation: Quantization of pmf (and cmf) with $V = 4$ bits

a	$p(a)$	$p(a) \cdot 2^4$	$p_V(a)$	$c_V(a)$
A	1/2	$16/2 = 8.00$	8	0
N	1/3	$16/3 \approx 5.33$	5	8
B	1/6	$16/6 \approx 2.66$	3	13

- Note: Quantized pmf $p_V(a)$ fulfills the requirement $\sum p_V(a) \leq 2^V$

Arithmetic Coding Example

Example: Step 1

s_n	p_V	c_V	parameter updates & output
initialization			$A_0 = 15 = \text{'1111'}$ $c_0 = 0 \text{ (")}$ $B_0 = 0 = \text{'0000 0000'}$ bitstream = ""
"B"	3	13	$A_0 \cdot p_V = 15 \cdot 3 = 45 = \text{'0010 1101'}$ $B_0 + A_0 \cdot c_V = 0 + 15 \cdot 13 = 195 = \text{'0 1100 0011'}$ $\Delta z = 2$
			$A_1 = \text{'1011'} = 11$ $c_1 = 0 \text{ (")}$ $B_1 = \text{'0000 1100'} = 12$ output = "11" bitstream = "11"

Arithmetic Coding Example

Example: Step 2

s_n	p_V	c_V	parameter updates & output
after step 1			$A_1 = 11 = \text{'1011'}$ $c_1 = 0 \text{ (')} $ $B_1 = 12 = \text{'0000 1100'}$ bitstream = "11"
"A"	8	0	$A_1 \cdot p_V = 11 \cdot 8 = 88 = \text{'0101 1000'}$ $B_1 + A_1 \cdot c_V = 12 + 11 \cdot 0 = 12 = \text{'0 0000 1100'}$ $\Delta z = 1$
			$A_2 = \text{'1011'} = 11$ $c_2 = 1 \text{ ('0')}$ $B_2 = \text{'0001 1000'} = 24$ output = "" bitstream = "11"

Arithmetic Coding Example

Example: Step 3

s_n	p_V	c_V	parameter updates & output
after step 2			$A_2 = 11 = \text{'1011'}$ $c_2 = 1 \quad (\text{'0'})$ $B_2 = 24 = \text{'0001 1000'}$ bitstream = "11"
"N"	5	8	$A_2 \cdot p_V = 11 \cdot 5 = 55 = \text{'0011 0111'}$ $B_2 + A_2 \cdot c_V = 24 + 11 \cdot 8 = 112 = \text{'0 0111 0000'}$ $\Delta z = 2$
			$A_3 = \text{'1101'} = 13$ $c_3 = 2 \quad (\text{'01'})$ $B_3 = \text{'1100 0000'} = 192$ output = "0" bitstream = "110"

Arithmetic Coding Example

Example: Step 4

s_n	p_V	c_V	parameter updates & output
after step 3			$A_3 = 13 = \text{'1101'}$ $c_3 = 2 \quad (\text{'01'})$ $B_3 = 192 = \text{'1100 0000'}$ bitstream = "110"
"A"	8	0	$A_3 \cdot p_V = 13 \cdot 8 = 104 = \text{'0110 1000'}$ $B_3 + A_3 \cdot c_V = 192 + 13 \cdot 0 = 192 = \text{'0 1100 0000'}$ $\Delta z = 1$
			$A_4 = \text{'1101'} = 13$ $c_4 = 3 \quad (\text{'011'})$ $B_4 = \text{'1000 0000'} = 128$ output = "" bitstream = "110"

Arithmetic Coding Example

Example: Step 5

s_n	p_V	c_V	parameter updates & output
after step 4			$A_4 = 13 = \text{'1101'}$ $c_4 = 3 \quad (\text{'011'})$ $B_4 = 128 = \text{'1000 0000'}$ bitstream = "110"
"N"	5	8	$A_4 \cdot p_V = 13 \cdot 5 = 65 = \text{'0100 0001'}$ $B_4 + A_4 \cdot c_V = 128 + 13 \cdot 8 = 232 = \text{'0 1110 1000'}$ $\Delta z = 1$
			$A_5 = \text{'1000'} = 8$ $c_5 = 4 \quad (\text{'0111'})$ $B_5 = \text{'1101 0000'} = 208$ output = "" bitstream = "110"

Arithmetic Coding Example

Example: Step 6

s_n	p_V	c_V	parameter updates & output
after step 5			$A_5 = 8 = \text{'1000'}$ $c_5 = 4 \quad (\text{'0111'})$ $B_5 = 208 = \text{'1101 0000'}$ bitstream = "110"
"A"	8	0	$A_5 \cdot p_V = 8 \cdot 8 = 64 = \text{'0100 0000'}$ $B_5 + A_5 \cdot c_V = 208 + 8 \cdot 0 = 208 = \text{'0 1101 0000'}$ $\Delta z = 1$
			$A_6 = \text{'1000'} = 8$ $c_6 = 5 \quad (\text{'01111'})$ $B_6 = \text{'1010 0000'} = 160$ output = "" bitstream = "110"

Arithmetic Coding Example

Example: Codeword Termination

s_n	p_V	c_V	parameter updates & output
after step 6			$A_6 = 8 = \text{'1000'}$ $c_6 = 5 \quad (\text{'01111'})$ $B_6 = 160 = \text{'1010 0000'}$ bitstream = "110"
final rounding			$B^* = \text{"1 0010 0000"}$ (rounding up B_6) bitstream = "1101 000" ($c_6 - 1$ inverted bits) $c = 1 \quad (\text{'0'})$ $B = \text{"0010 0000"}$
termination			final bitstream = "1101 0000 0" ($c + 1$ bits added)

→ Bitstream $\mathbf{b} = \text{"1101 0000 0"}$ (for sequence $\mathbf{s} = \text{"BANANA"}$)

→ Same number of bits ($K = 9$) as for Shannon-Fano-Elias coding

Decoding With Finite Precision

Identification of Intervals

- Important: Same rounding of interval width as in encoder ($A_n \mapsto A_{n+1}$)
- Arithmetic codeword “ $b_0b_1b_2b_3b_4b_5b_6\cdots$ ” represents binary fraction

$$v = (0.b_0b_1b_2b_3b_4b_5b_6\cdots)_b$$

- Iterative decoding: Output symbol s_n which fulfills inequality

$$L_n + W_n \cdot c(s_n) \leq v < L_n + W_n \cdot c(s_n) + W_n \cdot p(s_n)$$

Observation:

- Lower interval boundary L_n cannot be represented with reasonable precision
- ➔ Idea: Subtract L_n from the inequality
- ➔ Symbol s_n is identified by

$$W_n \cdot c(s_n) \leq v - L_n < W_n \cdot c(s_n) + W_n \cdot p(s_n)$$

- ➔ The value $u_n = v - L_n$ used in comparisons can be stored with $(U + V)$ bits, but needs to be updated after a symbol s_n is decoded

Arithmetic Decoding: Binary Representations

Analyse Binary Representations

$$W_n = 0. \overbrace{0000000000000000 \dots 0}^{z_n - U} \overbrace{1xx \dots x}^U 0000000000000000 \dots$$

$$W_n \cdot (c_V + p_V) = 0. \overbrace{0000000000000000 \dots 0}^{z_n - U} \overbrace{xxx \dots xxxxxxx}^{U + V} 000000000 \dots$$

$$v - L_n = 0. \overbrace{0000000000000000 \dots 0}^{z_n - U} \overbrace{xxx \dots xxxxxxx}^{U + V} \overbrace{xxxxxxxxx \dots}^{U + V}$$

$$W_n \cdot c_V = 0. \overbrace{0000000000000000 \dots 0}^{z_n - U} \overbrace{xxx \dots xxxxxxx}^{U + V} 000000000 \dots$$

- ➔ Use $(U+V)$ -bit integer u_n in comparisons (down-rounded value of $v - L_n$)
 - Initialization: $u_n =$ (first $U+V$ bits from bitstream)
 - Update $u_n \mapsto u_{n+1}$
 - ➔ Subtract lower boundary: $u_n = u_n - W_n \cdot c_V(s_n)$
 - ➔ Align with interval width: $u_n = u_n \ll \Delta z$ (leading zeros in $A_n \cdot p_V(s_n)$)
 - ➔ Fill least significant bits with next Δz bits from bitstream

Overview of Arithmetic Decoding Process

Arithmetic Decoding

- 1 Initialization: $A_0 = 2^U - 1$,
 $u_0 = (\text{first } U+V \text{ bits from bitstream})$
- 2 Iteration: For $n = 0, 1, \dots, N - 1$, do
 - a **Identify next symbol:** For $k = 0, 1, \dots$, do *(loop over alphabet)*
 - Calculate upper boundary $U(a_k) = A_n \cdot (c_V(a_k) + p_V(a_k))$
 - If $u_n < U(a_k)$, then
 - Output next symbol $s_n = a_k$
 - break loop over k
 - b **Update parameters:**
 - Calculate intermediate value: $A^* = A_n \cdot p_V(s_n)$
 - Determine number Δz of trailing zeros in $(U+V)$ -bit integer A^*
 - $A_{n+1} = A^* \gg (V - \Delta z)$
 - $u_{n+1} = (u_n \ll \Delta z) + (\text{next } \Delta z \text{ bits from bitstream})$

Arithmetic Decoding Example

Decode Bitstream obtained in Encoding Example

- Coding example (see encoding example)
 - IID source with symbol alphabet $\{A, N, B\}$
 - Pmf is given by $\{1/2, 1/3, 1/6\}$
 - Arithmetic coding with $V = 4$ and $U = 4$
- Quantized pmf (and cmf) with $V = 4$ bits

a	$p(a)$	$p(a) \cdot 2^4$	$p_V(a)$	$c_V(a)$
A	$1/2$	$16/2 = 8.00$	8	0
N	$1/3$	$16/3 \approx 5.33$	5	8
B	$1/6$	$16/6 \approx 2.66$	3	13

- Bitstream $\mathbf{b} = \text{"1101 0000 0"}$

Arithmetic Decoding Example

Example: Step 1

a	c_V	p_V	decoding & update	output
initialization			bitstream = “ 1101 0000 0(000 0000 0...)” $A_0 = 15 = \text{'1111'}$ $u_0 = 208 = \text{'1101 0000'}$	B
A	0	8	$U(A) = 15 \cdot (0 + 8) = 120 \rightarrow U(A) \leq u_0$	
N	8	5	$U(N) = 15 \cdot (8 + 5) = 195 \rightarrow U(N) \leq u_0$	
B	13	3	$U(B) = 15 \cdot (13 + 3) = 240 \rightarrow U(B) > u_0$	B
			$A^* = 15 \cdot 3 = 45 = \text{'0010 1101'}$ $u^* = 208 - 15 \cdot 13 = 13 = \text{'0000 1101'}$ $\Delta z = 2$ $A_1 = \text{'1011'} = 11$ $u_1 = \text{'0011 0100'} = 52$ bitstream = “ 1101 0000 0(000 0000 0...)”	

Arithmetic Decoding Example

Example: Step 2

a	c_V	p_V	decoding & update	output
after step 1			bitstream = "1101 0000 0(000 0000 0...)" $A_1 = 11 = \text{'1011'}$ $u_1 = 52 = \text{'0011 0100'}$	A
A	0	8	$U(A) = 11 \cdot (0 + 8) = 88 \quad \rightarrow U(A) > u_1$	
N	8	5	$U(N) = 11 \cdot (8 + 5) = 143$	
B	13	3	$U(B) = 11 \cdot (13 + 3) = 176$	
			$A^* = 11 \cdot 8 = 88 = \text{'0101 1000'}$ $u^* = 52 - 11 \cdot 0 = 52 = \text{'0011 0100'}$ $\Delta z = 1$ $A_2 = \text{'1011'} = 11$ $u_2 = \text{'0110 1000'} = 104$ bitstream = "1101 0000 0(000 0000 0...)"	

Arithmetic Decoding Example

Example: Step 3

a	c_V	p_V	decoding & update	output
after step 2			bitstream = “1101 0000 0(000 0000 0⋯)” $A_2 = 11 = \text{'1011'}$ $u_2 = 104 = \text{'0110 1000'}$	N
A	0	8	$U(A) = 11 \cdot (0 + 8) = 88 \rightarrow U(A) \leq u_2$	
N	8	5	$U(N) = 11 \cdot (8 + 5) = 143 \rightarrow U(N) > u_2$	
B	13	3	$U(B) = 11 \cdot (13 + 3) = 176$	
			$A^* = 11 \cdot 5 = 55 = \text{'0011 0111'}$ $u^* = 104 - 11 \cdot 8 = 16 = \text{'0001 0000'}$ $\Delta z = 2$ $A_3 = \text{'1101'} = 13$ $u_3 = \text{'0100 0000'} = 64$ bitstream = “1101 0000 0(000 0000 0⋯)”	

Arithmetic Decoding Example

Example: Step 4

a	c_V	p_V	decoding & update	output
after step 3			bitstream = "1101 0000 0(000 0000 0...)" $A_3 = 13 = \text{'1101'}$ $u_3 = 64 = \text{'0100 0000'}$	A
A	0	8	$U(A) = 13 \cdot (0 + 8) = 104 \rightarrow U(A) > u_3$	
N	8	5	$U(N) = 13 \cdot (8 + 5) = 169$	
B	13	3	$U(B) = 13 \cdot (13 + 3) = 208$	
			$A^* = 13 \cdot 8 = 104 = \text{'0110 1000'}$ $u^* = 64 - 13 \cdot 0 = 64 = \text{'0100 0000'}$ $\Delta z = 1$ $A_4 = \text{'1101'} = 13$ $u_4 = \text{'1000 0000'} = 128$ bitstream = "1101 0000 0(000 0000 0...)"	

Arithmetic Decoding Example

Example: Step 5

a	c_V	p_V	decoding & update	output
after step 4			bitstream = "1101 0000 0(000 0000 0...)" $A_4 = 13 = \text{'1101'}$ $u_4 = 128 = \text{'1000 0000'}$	N
A	0	8	$U(A) = 13 \cdot (0 + 8) = 104 \rightarrow U(A) \leq u_4$	
N	8	5	$U(N) = 13 \cdot (8 + 5) = 169 \rightarrow U(N) > u_4$	
B	13	3	$U(B) = 13 \cdot (13 + 3) = 208$	
			$A^* = 13 \cdot 5 = 65 = \text{'0100 0001'}$ $u^* = 128 - 13 \cdot 8 = 24 = \text{'0001 1000'}$ $\Delta z = 1$ $A_5 = \text{'1000'} = 8$ $u_5 = \text{'0011 0000'} = 48$ bitstream = "1101 0000 0(000 0000 0...)"	

Arithmetic Decoding Example

Example: Step 6 (last symbol)

a	c_V	p_V	decoding & update	output
after step 5			bitstream = "1101 0000 0(000 0000 0...)" $A_5 = 8 = \text{'1000'}$ $u_5 = 48 = \text{'0011 0000'}$	
A	0	8	$U(A) = 8 \cdot (0 + 8) = 64 \quad \rightarrow U(A) > u_4$	A
N	8	5	$U(N) = 8 \cdot (8 + 5) = 104$	
B	13	3	$U(B) = 8 \cdot (13 + 3) = 128$	

bitstream "1101 0000 0" \iff symbol sequence "BANANA"

Note: Required some bits after end of the bitstream

- For non-prefix variant: Use bits equal to 0
- For prefix-free variant: Any bit values (0 or 1) can be used

Efficiency of Arithmetic Coding

Increase in Codeword Length relative to Shannon-Fano-Elias Coding

- Excess rate due to rounding of interval width

$$\Delta \ell = \lceil -\log_2 W_N \rceil - \lceil -\log_2 p(\mathbf{s}) \rceil < 1 + \log_2 \frac{p(\mathbf{s})}{W_N} \quad (48)$$

- Upper bound for increase in codeword length per symbol relative to infinite-precision Shannon-Fano-Elias coding

$$\Delta \bar{\ell} < \frac{1}{N} + \log_2 (1 + 2^{1-U}) - \log_2 \left(1 - \frac{2^{-V}}{p_{\min}} \right) \quad (49)$$

(for a derivation see WIEGAND, SCHWARZ, page 51-52)

- Example:

- Number of coded symbols $N = 1000$,
- Arithmetic precision: $V = 16$ and $U = 12$,
- Minimum probability mass $p_{\min} = 0.02$
- ➔ Increase in codeword length is less than 0.003 bit per symbol

Complexity Reduction: Binary Arithmetic Coding

Binary Arithmetic Coding

- Most popular type of arithmetic coding: JPEG 2000, H.264, H.265
- Binarization of $S \in \{a_0, a_1, \dots, a_{M-1}\}$ produces $C \in \{0, 1\}$
 - ➔ Any prefix code can be used for binarization
 - ➔ Example: Truncated unary binarization

S_n	number of bins B	C_0	C_1	C_2	\dots	C_{M-2}	C_{M-1}
a_0	1	1					
a_1	2	0	1				
\vdots	\vdots	\vdots	\vdots	\ddots			
a_{M-2}	$M-2$	0	0	0	\dots	0	1
a_{M-1}	$M-2$	0	0	0	\dots	0	0

- ➔ Entropy unchanged due to binarization $\mathbf{S} \mapsto \mathbf{C}$

$$H(\mathbf{S}) = \mathbb{E}\{-\log_2 p(\mathbf{S})\} = \mathbb{E}\{-\log_2 p(\mathbf{C})\} = H(\mathbf{C})$$

Practical Arithmetic Coding

Complexity Reduction

- Binary arithmetic coding
- Multiplication-free implementations
- Bypass mode: Low-complexity coding of bins with $p = 0.5$

Practical Design Aspects

- 1 Context selection
 - Use reasonable context variables $X = f(S_{n-1}, S_{n-2}, \dots)$ for switching probability tables $p(a | X)$
 - Use context switching only when useful (certain bins)
- 2 Estimate probabilities during coding
 - Choose appropriate “window sizes” for estimation
- 3 Suitably combine context selection and probability estimation

Experimental Comparison of Lossless Coding Techniques

Example: Markov Source

- Stationary Markov source given by conditional pmf

a	$p(a a_0)$	$p(a a_1)$	$p(a a_2)$
a_0	0.90	0.15	0.05
a_1	0.05	0.80	0.05
a_2	0.05	0.05	0.60

$$H(S) = 1.2575$$

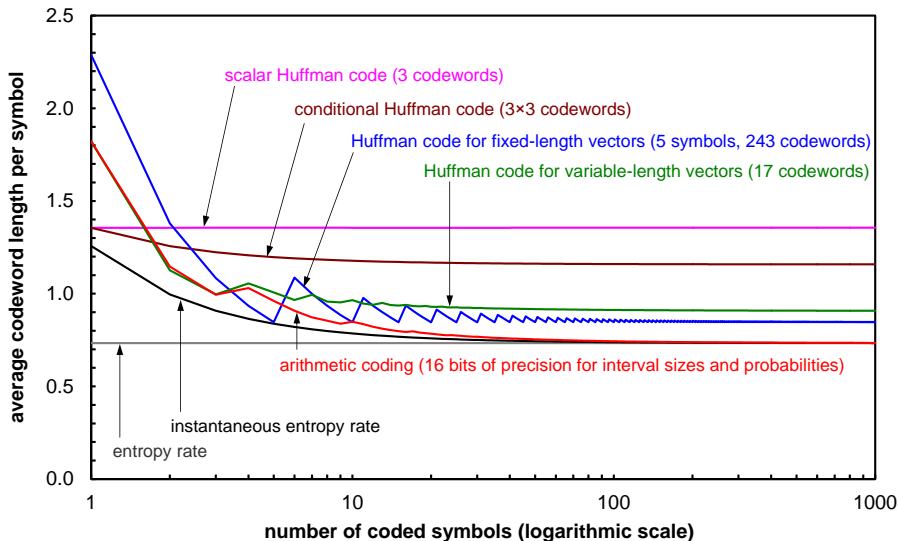
$$\bar{H}(S) = 0.7331$$

- Bounds for lossless coding
 - Entropy rate $\bar{H}(S)$ for coding of infinitely many symbols
 - Instantaneous entropy rate $\bar{H}_{\text{inst}}(S, L)$ for coding L symbols

$$\bar{H}_{\text{inst}}(S, L) = \frac{1}{L} H(S_0, S_1, \dots, S_{L-1}) \quad (50)$$

- Coding experiment
 - Coding of 1 000 000 realizations of example stationary Markov source
 - Calculate average codeword length for sequences of 1 to 1000 symbols

Experimental Results



Part Summary

Uniquely decodable codes & bounds for lossless coding

- Kraft inequality, prefix codes
- Scalar entropy, conditional entropy, block entropy
- Entropy rate, instantaneous entropy rate

Variable-length codes for scalars and vectors

- Optimal code for given pmf: Huffman code
- Scalar, conditional codes: Inefficient for pmfs with $p(a) \gg 0.5$
- Block codes and V2V codes: Code tables can become extremely large
- Difficult adaptation to instationary sources

Arithmetic coding

- No codeword table: Iterative construction of codeword
- Close to entropy bound for $N \gg 1$
- Well suited for exploiting statistical dependencies
- Well suited for adapting probabilities during coding